

Product information

Decentral

Periphery Block

DP301C



(valid from version DP301C-02)

Changes to older versions of this document

- Rev. 01 → 02:** Number format of PWM ans TC data added
Rev. 02 → 03: Pinning DI 21 + DI 22 corrected (was 20 resp. 21)
Rev. 03 → 04: Link to Security Guideline added

Description

Decentral periphery module for 35mm DIN Rail

DP303C with

- fixed periphery
- communication to PLC with a protocol compatible to CANopen®
- with switchable terminate resistors for CAN

State LEDs for Error, Run

Onboard-Periphery

12 digital I/Os
thereof 2 PWM-Outputs
thereof 2 PushPull-Outputs

12 digital Inputs
thereof 2 Counters 1kHz
thereof 2 Counters 100kHz

3 analog I/O's
((switchable per channel as input/output and for U / I)

3 analog Inputs
(customizable as RTD/ U/ I/ TC/ strain gauge)

Inserting stripes
- for Logo and identification
(thereby customized adaption possible easy)

Scope of delivery:

- Grounding terminal
- Technical data sheet

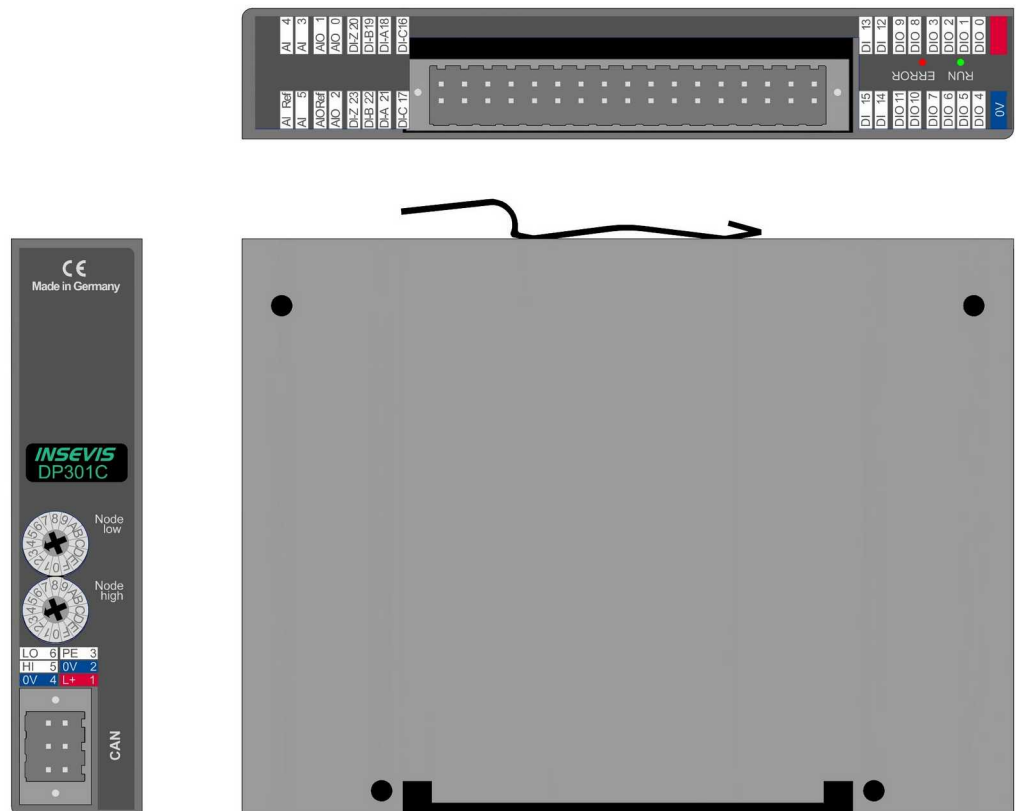


Figure above: View to rear side and connections sides of DP301C (horizontal use)

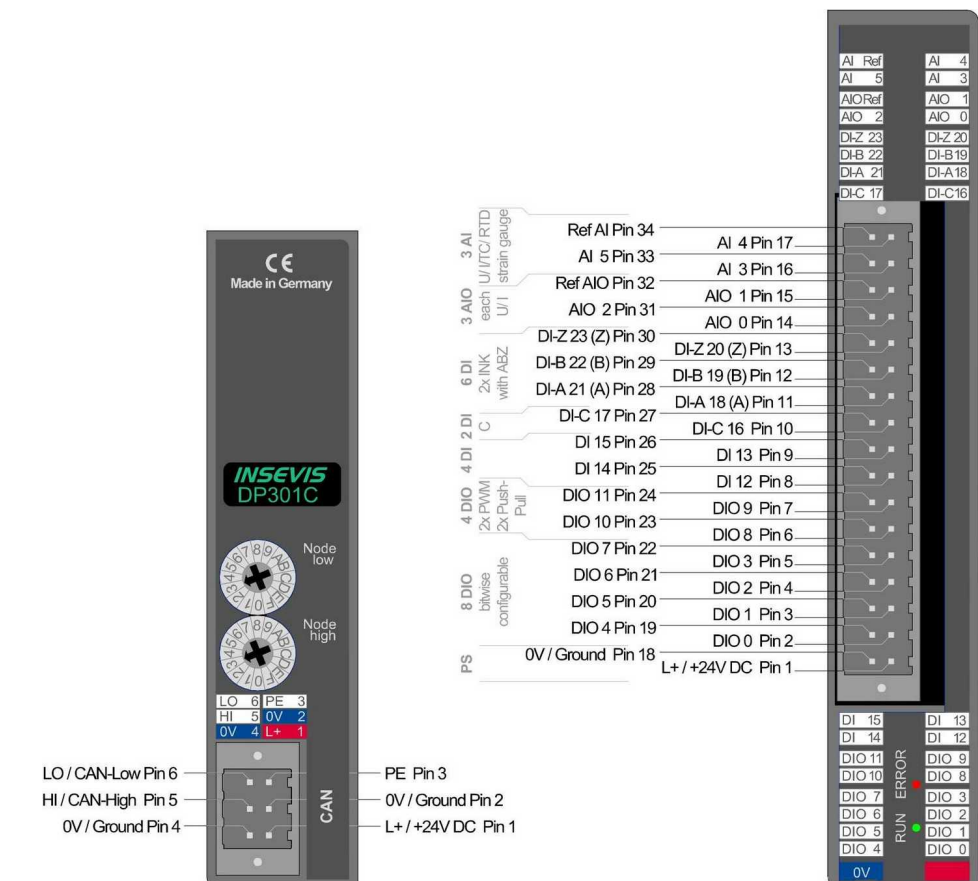


Figure above: Periphery connectors of decentral periphery module DP301C

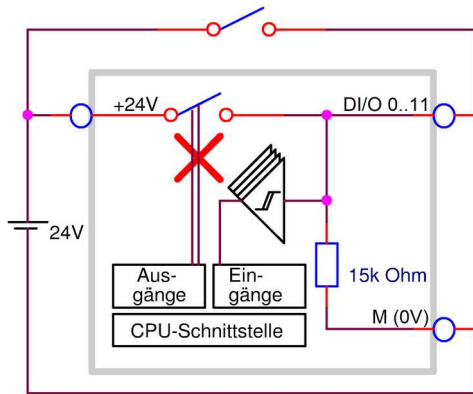
Technical data	
Dimensions W x H x D (mm) mounting, IP-protection class, weight	20 x 116 x 87 on 35mm DIN rail IP41 ca. 500g
Betriebstemperaturbereich Lagertemperaturbereich	20°C ... +60°C (without condensation) -30°C ... +80°C
Connection technology	removable connector 2 bolt flanges or lift arms (cage clamp technology) for cross section up to max. 1,5mm ²
Load voltage L+	24V DC (11 V ... 30V DC)
Current consumption	200mA
Start-up current	< 3A
CAN (protocols)	compatible to CANopen® 2 RxPDO, 4 TxPDO, SDO, NMT, 10 kBaud ... 1 MBaud
Onboard periphery	12 dI/O, 12 dI (C), 3 aI/O (U/ I), 3 al (U/ I/ RTD/ TC/ strain gauge)

Technical data	digital Inputs/ Outputs (DIO 0...11 and DI 12...23)		
Load voltage L+ Current consumption Power loss Diagnostic LEDs	10 V ... 30 V DC 10 mA (without load) internally limited none	Cable length - unshielded (max.) - shielded (max.)	30 m 100 m
Digital inputs/outputs	8 outputs (DIO 0...7) (each with read-back input) 2x2 push-pull outputs (DIO 8...11) (can be switched off in pairs) (each with read-back input: * → do not apply +24VDC here! *	Outputs: Switch-on delay Switch-off delay Function of the push-pull outputs Number format PWM uni 0 ... 100% PWM bidir -100%..0%..100%	50 µs (type.) 30 µs (typ., without load) output PWM (0..100%) PWM + direction bit (0 .. +/- 100%) 0000...6C00 (hex) 9400 ... 0000 ... 6C00 (hex)
Output current for signal 0 for signal 1 Input current for signal 1	0,5 mA (max.) 0,5 A (max. up to 60°C) 1 mA (type)	Max. switching frequency of the outputs	100 Hz (with resistive load)
Signal level of the outputs for signal 0 for signal 1 Input voltage for signal 0 for signal 1	1,0 V at 500 Ω (max.) L+ - 1,0 V at 0,5 A load (min.) 0V ... +5 V +10,5V ... +30 V	Max. switching frequency of the push/pull PWM outputs	50 kHz (with resistive load)
Digital inputs	4 inputs (DI 12...15)	Inputs: Switch-on delay Switch-off delay	1,5 ms 4,5 ms
Function slower counter	2 up counters (DI-C 16...17) 16 bit optionally as counter value or frequency [Hz]	Function fast counters	2 counters (DI-A/B/Z 18...22) 16 bit Incremental encoder 4-fold or up-counter Optionally with zero-track reference function
max. frequency slow counter	1 kHz	max. frequency at input pins high-speed counters	100 kHz
Open-circuit monitoring, fault diagnosis Potential isolation to the PLC DO 8...11: Short-circuit protection against 0V	no no no*	Total current	2 A (max. up to 60°C)

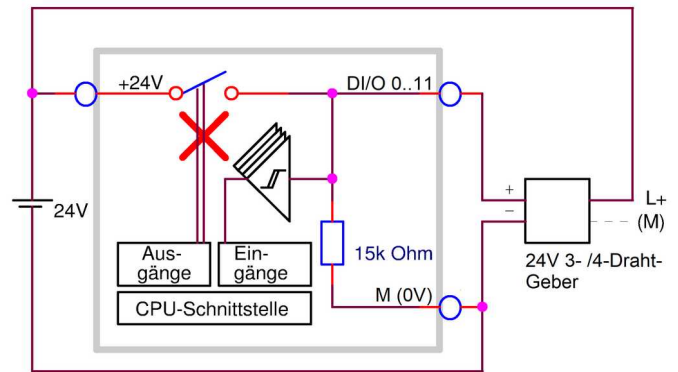
*** Warning of risk of damage when using DIO 8...11 as input/output**

→ If DIO 8...11 is in PWM uni, PWM bidir or input/output mode, +24VDC must NOT be applied!

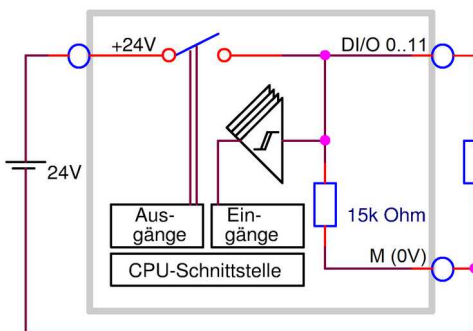
Block diagrams digital inputs/outputs (DIO 0...7)



Block diagram of the DIOs only as input for 2-wire encoders



Block diagram of the DIOs for 3-/4-wire encoders



Block diagram of the DIOs as read-back output

Input address			
Digital:	Start	End	
Analog:	0	15	
	128	139	

Output address			
Digital:	Start	End	
Analog:	0	7	
	128	133	

Digital input and output			
	Input address	Output address	
	Disable the output		
DIO 0:	0.0	☐	0.0
DIO 1:	0.1	☐	0.1
DIO 2:	0.2	☐	0.2
DIO 3:	0.3	☐	0.3
DIO 4:	0.4	☐	0.4
DIO 5:	0.5	☐	0.5
DIO 6:	0.6	☐	0.6
DIO 7:	0.7	☐	0.7

Configuration block start/end addresses (in bytes) and I/O assignment in the ConfigStage

Configuration of the PWM outputs (DIO 8...11)

Push-pull outputs or PWM (setting via ConfigStage)

Channel 1.0/1.1 and 1.2/1.3 in pairs

Configuration "Input"

→ both outputs deactivated

Configuration "Input/Output"

→ 2 read-back outputs
(active high AND low switching !)

Configuration "PWM uni"

→ DIO 8 resp. DIO 10 deactivated as bit
Set value of the PWM duty cycle
in the specified output word,
Specification of the frequency constant
→ DIO 9 resp. DIO 11 as output (can be read back)

Configuration „PWM bidir“

→ DIO 8 resp. DIO 10 as uni, but setpoint with sign
→ DIO 9 resp. DIO 11 as direction bit (= sign)

Digital input and output or PWM output

	Mode	Input address	Output address	Duty cycle Output address	Frequency [Hz]
DIO 8:	Input	1.0			
DIO 9:	Input	1.1			
DIO 10:	Input	1.2			
DIO 11:	Input/Output	1.3			
DI 12:	PWM uni	1.4			
DI 13:	Input	1.5			
DI 14:	Input	1.6			
DI 15:	Input	1.7			

Notes:

When DIO 8 is in "PWM uni" or "PWM bidir" mode, DI-A 21 is only available in "Input" mode.
When DIO 10 is in "PWM uni" or "PWM bidir" mode, DI-A 18 is only available in "Input" mode.

Warning:

When DIO8, DIO10 is in "PWM uni", "PWM bidir" or "Input/Output" mode, should not connect to 24V. Risk of damage.

Configuration of the counter inputs (DI-C 16/17 and DI-A/B/Z 18..23)

Slow counters (setting via ConfigStage)

Configuration "Input"

→ DI-C 16 resp DI-C 17 are normal inputs,
Counter is switched off

Configuration "counting up"

→ On DI-C 16 resp DI-C 17, rising edges are counted

Address of the counter word and the reset bit are displayed
(input bit switched off)

Digital input or Low speed counter

	Mode	Input address	Counter word input address	Reset bit output address
DI-C 16:	Input	2.0		
DI-C 17:	Input	2.1		
	Counter up			
	Frequency measure			

"Frequency measurement" configuration

→ The frequency [Hz] is output instead of the counter value

Fast counters (setting via ConfigStage)

Digital input or High speed counter

	Mode	Input address	Counter word input address	Reset bit output address	Enable bit referencing output address
DI-A 18:	Input	2.2			
DI-B 19:	Input	2.3			
DI-Z 20:	Count up	2.4			
	Up/Down (Pulse/Dir)				
	Up/Down (Pulse/Dir/Zero)				
DI-A 21:	Encoder (x4)	2.5			
DI-B 22:	Encoder (x4) Zero	2.6			
DI-Z 23:		2.7			

Notes:

When DI-A 18 is in "Counter" or "Encoder" mode, DIO 10 is only available as an "Input" or "Input/Output."
When DI-A 21 is in "Counter" or "Encoder" mode, DIO 8 is only available as an "Input" or "Input/Output."

Configuration „Input“

→ DI-A 18, DI-B 19, DI-Z 20 resp. DI-A 21, DI-B 22, DI-Z 23 are
normal Inputs,
Counter is switched off

Configuration "counting up"

→ Rising edges are counted on DI-A 18 resp. DI-A 21 ,
→ the other signals (B and Z) are inputs

Configuration "counting up/down"

→ rising edges are counted on channel DI-A 18 resp. DI-A 21 and
→ Channel DI-B 19 resp. DI-B 22 serves as a direction bit (0=backwards, 1=forwards)
→ Z is input

Configuration "Encoder x4"

→ DI-A 18/DI-B 19 resp. DI-A 21/DI-B 22 form an encoder input with quadruple evaluation
→ Z is input

Configuration "counting up/down zero" "Encoder x4 zero"

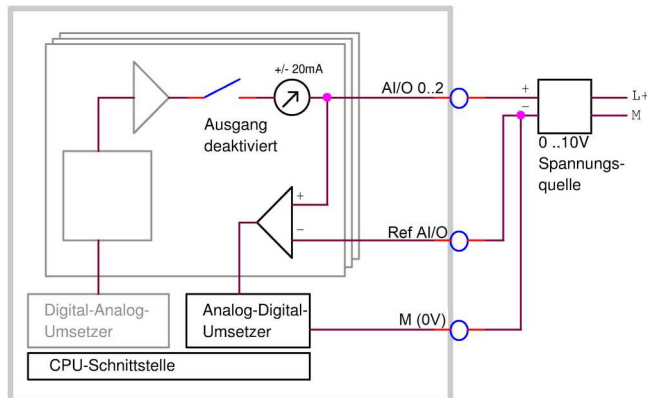
→ In addition, the Z input can be activated via the "enable reference" bit:
On a rising edge at the Z bit, the counter is reset to 0 and the enable reference bit is deleted.
Notes on using the counter inputs

Notes on using the counter inputs (DI-C 16/17 and DI-A/B/Z 18..23)

- Counters can only be set (and held) to zero by setting the reset bit (static)
- the configuration cannot be changed at runtime under Step7:
- all addresses are specified as an offset in relation to the configured start address

Technical data		analog Inputs/Outputs U/I (AIO 0...2)	
Load voltage L+	- (internal supply)	Cable length - unshielded (max.) - shielded (max.)	30 m 100m
Analogue inputs Input ranges	3 (alternatively configurable to outputs via software) 0...20 mA, 4...20 mA, +/- 20mA 0...10 V	Permissible voltage between inputs and A-GND (max.)	-1 V ... + 24 V DC
Diagnostic LEDs	none	Error message if range is exceeded	Parameterizable diagnostic and limit value alarms on request
Number format +/- 20mA: -20 mA ... 20 mA otherwise	9400 ... 0000 ... 6C00 (hex) 0000 ... 6C00 (hex)	Wire break monitoring	by exceeding or falling below the measuring range (for 4...20 mA)
Overload range	20 mA ... 22 mA 10V ... 11,3 V	Signal transmitter connection type	unbalanced to A-GND (single ended)
Input resistance	0Ω (typ.) for measuring range current 1MΩ (typ.) for voltage measuring range	Measuring principle/ Conversion principle Resolution	successive approximation 12 Bit
Abtastzykluszeit = IntegrationszeitSampling cycle time = Integration time	parameterizable 1ms ... 35767 ms default: 100 ms (= mains frequency filter 50Hz and 60Hz)	Accuracy (related to measuring range)	< 1%
Analog outputs Output ranges	3 (alternatively to inputs configurable via software) 0(4)...20mA, 0...10V	Number format	0000 ... 6C00 (hexadecimal)
Output resolution	12 Bit	Short-circuit protection	yes
Diagnostic LEDs	none	Overdrive range	20 ... 23 mA 10 ... 11,3 Vsettling timeSettling time:
Settling time:	Time constant t (typ) 1,5 ms	Short-circuit current (typ.)	20 mA (at 10V) 32 mA (at mA)
Load resistor/ load resistor to A-GND	mA: 500 Ω (max.) V: 1 kΩ (min.)	Accuracy (related to measuring range)	< 1%

Block diagrams analog inputs/outputs U/I (AIO 0...2)



Block diagram of the analog inputs for 0 ... 10 V

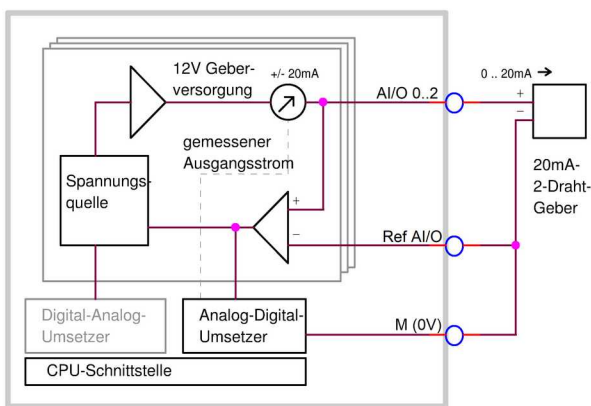
Analog Input and Output

Integration time [ms]: 100

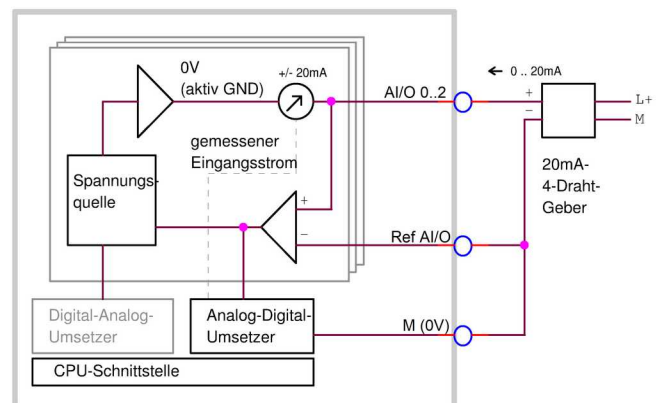
	Mode	Type	Address
AIO 0:	Input	0...10V	128
AIO 1:	Output	0...10V	130
AIO 2:	Input	0...10V	132

0...10V
0...20mA (2-wire)
4...20mA (2-wire)
4...20mA (4-wire)
+/- 20mA (4-wire)

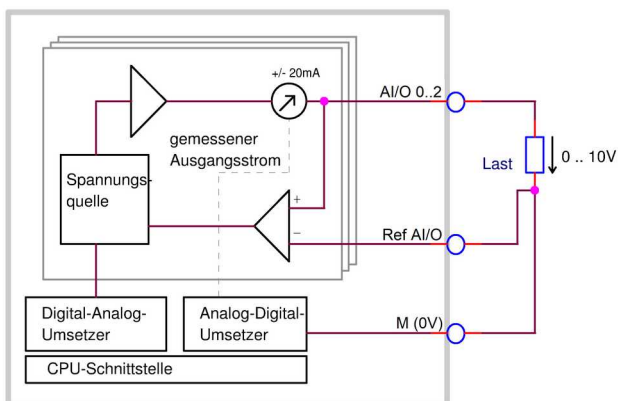
Configuration block addresses (in bytes)
and measuring range configuration Analogue I/O in the ConfigStage



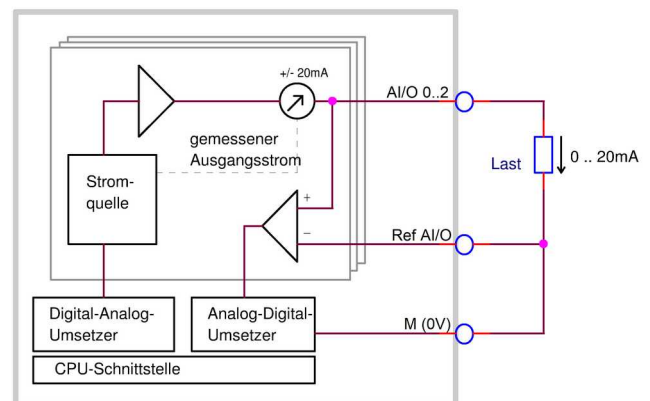
Block diagram of the analogue inputs for 20 mA
with 2-wire encoder



Block diagram of the analogue inputs for 20 mA
with 3/4-wire encoder



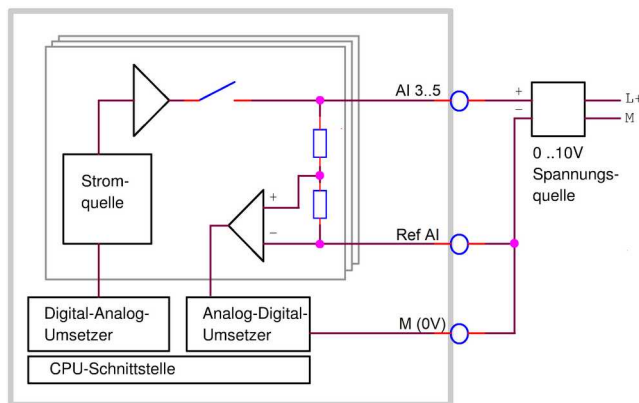
Block diagram of the analogue outputs for 10 V



Block diagram of the analogue outputs for 20 mA

Technical data	Analog Inputs U/ I/ RTD/ TC/ Strain gauge (AI 3...5)		
Load voltage L+	- (internal supply)	Cable length - unshielded (max.) - shielded (max.)	30 m 100m
Input ranges	0...20 mA, 4...20 mA, 0..10 V, DMS, PT100, PT1000, 2 kOhm, TC Typ K, J, N possible combinations: - 3x 20 mA or 10 V - 1x DMS - 3x PT100/PT1000/R (2 wire) - 1x PT100/PT1000/R (3 wire) + 1x PT100/PT1000/R (2 wire) + - 2x ThermoCouples, 1x PT	Permissible voltage between inputs and A-GND (max.)	-1 V ... + 24 V DC
Diagnostic LEDs	none	Error message if range is exceeded	Parameterizable diagnostic and limit value alarms on request
Number formats: 0 (4) .. 20mA /10V: PT100 standard / TC PT100 clima / PT1000 clima 2 kOhm	0 ... 6C00 (hex) 1/10 °C 1/100 °C 0 ... 2000 (dez)	Wire break monitoring	due to overrange or underrange
TC numerical Limits Overflow > 1622°C Underflow< F0C4 (hex) (transmitter error)	7FFF (hex) 8000 (hex)	TC-Terminal point compensation:	External: connect PT100/PT1000 on AI 5 Internal: connect AI 5 to AI Ref instead of PT100/PT1000 → usage of PCB temperature
Overdrive range	20 mA ... 22 mA 10V ... 11,3 V	Connection type of the signal transmitter Measuring ranges mA, V: otherwise:	unbalanced (single ended) against 0V potential-free (floating)
Input resistance	70Ω (typ.) for measuring range mA 1MΩ (typ.) otherwise	Measuring principle/ Conversion principle Resolution	Sigma Delta 16 Bit
Sampling cycle time = Integration time	parameterizable default: 100 ms depending on the measuring range	Accuracy (related to measuring range)	< 1%

Block diagrams analog inputs U/I / RTD/ TC/ Strain gauge (AI 3...5)



Block diagram of the analog inputs for 0 ... 10 V

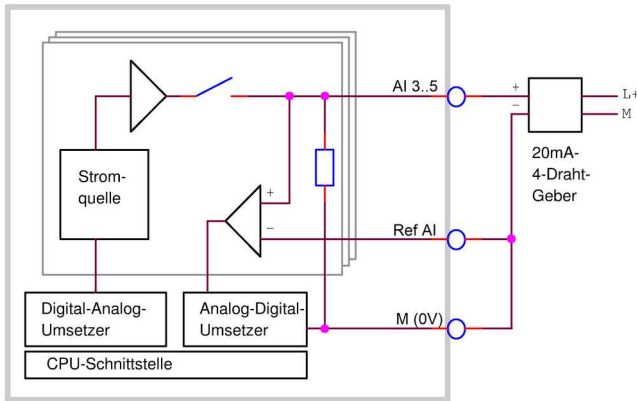
Analog Input

Integration time [ms]: 100

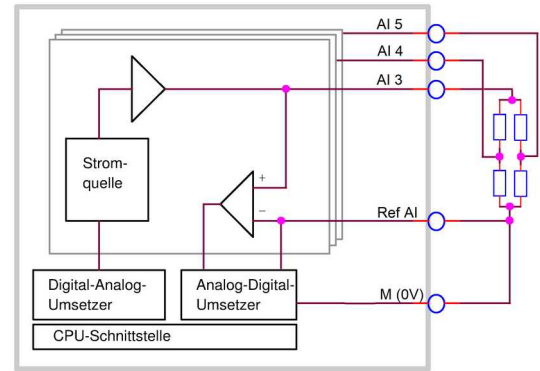
Mode: ☒ Voltage / Current (0..10V, 0..20mA, 4..20mA)
☐ Temperature (PT100, PT1000, TC)

	Type	Address
AI 3:	0..10V	134
AI 4:	0..10V	136
AI 5:	0..20mA (4-wire) 4..20mA (4-wire) strain gauge	138

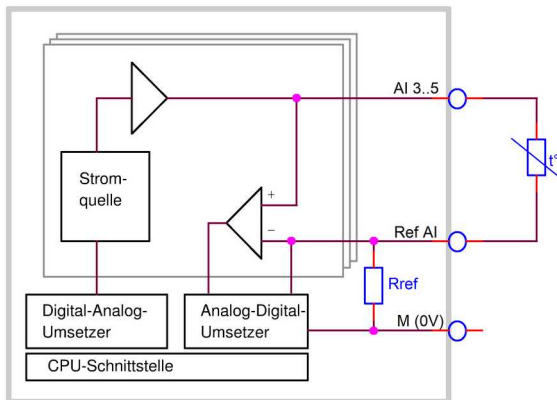
Configuration block addresses (in bytes)
and measuring range configuration in the ConfigStage
Voltage/current mode



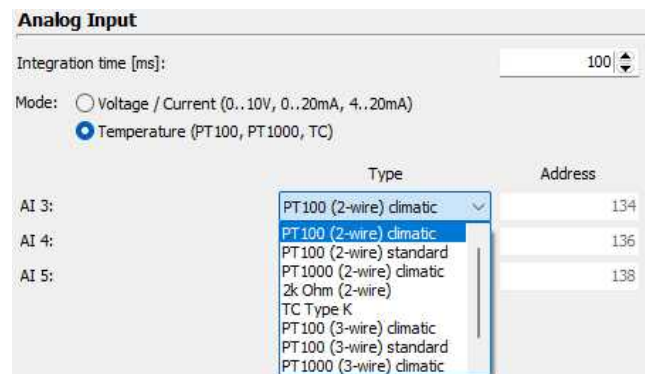
Block diagram of the analog inputs for 20 mA with 3/4 wire encoder



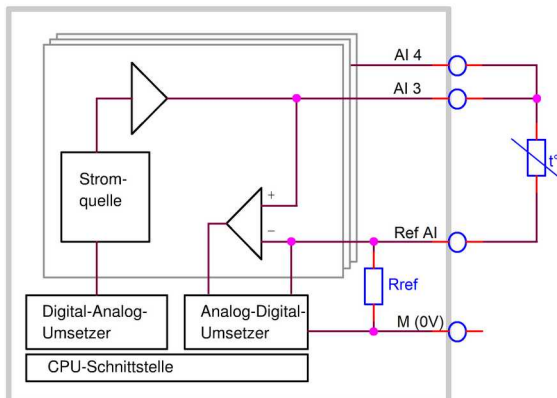
Block diagram of the analog inputs for strain gages (strain gauge)



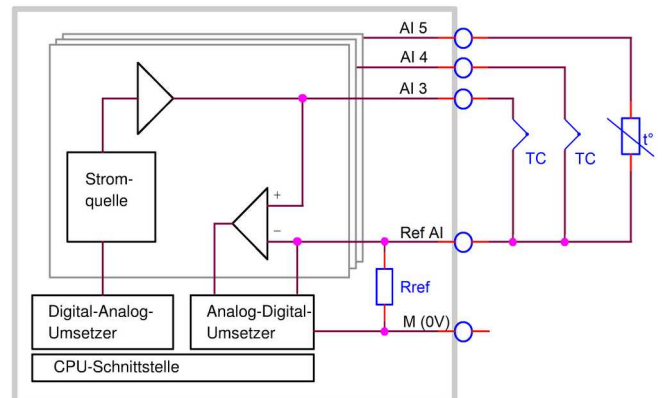
Block diagram of the analog inputs for PT100/PT1000/R 2-wire



Configuration block addresses (in bytes) and measuring range configuration Analog Input in the ConfigStage Mode temperature



Block diagram of the analog inputs for PT100/PT1000/R 3-Wire



Block diagram of the analog inputs for thermocouples with Terminal compensation



For terminal point compensation:

- either with PT100/1000 temperature at the terminal point or
- Connect AI5 (Pin33) to Ref AI (Pin34) (less accurate)

Assignment of the process image:

Assignment of the process image: Digital inputs The module occupies 16 bytes in the process image (from the configured start address).			
Offset	I/O	Function	Description
0..2	I	Input DI0.0..DI2.7	Input bits (possibly blocked - depending on configuration)
3	I	reserved	
4,5	I	Slow counter 0	Counter word (16-bit high-endian), measuring range depending on configuration
6,7	I	Slow counter 1	Counter word (16-bit high-endian), measuring range depending on configuration
8..11	I	Fast counter 0	Counter word (32-bit high-endian), measuring range depending on configuration
12..15	I	Fast counter 1	Counter word (32-bit high-endian), measuring range depending on configuration

Assignment of the process image: Digital outputs The module occupies 8 bytes in the process image (from the configured start address).			
Offset	I/O	Function	Description
0	O	Output DO0..DO7	Standard-PLC outputs
1	O	Output DO8..DO11	4 push/pull outputs (4 bits unused)
2	O	Resetbits slow counter	.0 Reset Counter 0, .1 Reset Counter1
3	O	Reset-/Controlbits fast counter	.0 Reset Counter 0, .1 Reset Counter1 .2 EnableRef Counter 0, .3 EnableRef Counter1
4,5	O	PWM 0 target value	(16 Bit high-endian)
6,7	O	PWM 1 target value	(16 Bit high-endian)

Assignment of the process image: Analog inputs The module occupies 6 input words in the process image (from the configured start address).			
Offset	I/O	Function	Description
0,2,4	I	Input AI0..AI2	Measurement range depending on configuration
6,8,10	I	Input AI3..AI5	Measurement range depending on configuration

Assignment of the process image: Analog outputs The module occupies (from configured start address) 3 output words in the process image.			
Offset	I/O	Function	Description
0,2,4	O	Output AO0..AO2	Measurement range depending on configuration

Ordering data of devices

Identification	Standard
Decentral periphery module DP301C	DP301C-02

Ordering data of accessoires

Identification / Order-No.
Connector 2x3pin (bolt flanges) / E-CONS06-00
Connector 2x17polig (lift arms) / E-CON34-00
Connector 2x17polig (bolt flanges) / E-CONS34-00

Qualified personnel

All devices described in this manual may only be used, built up and operated together with this documentation. Installation, initiation and operation of these devices might only be done by instructed personnel with certified skills, who can prove their ability to install and initiate electrical and mechanical devices, systems and current circuits in a generally accepted and admitted standard.

Manuals, sample programs

Additional documentation by manuals is available as well sample applications at the download area of www.insevis.com in English language for free download.

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Attention: The deletion of personal data on the old devices to be disposed of is the responsibility of the end user.